**Report week 3**

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1)**Mono alternating rectifier**

D1 in out diode

.model diode D tt=5e-9

R1 out 0 100

v1 in 0 dc 5 sin(0 10 50 0 0)

->**TR** analysis(in time)

Graphical user interface, application

Description automatically generated

->**DC** analysis

Graphical user interface

Description automatically generated

When we have the input signal : positive passes through the diode

Negative doesn’t(because it is closed)

2)**Double alternating rectifiers**

Rectifier

D1 in out diode

D2 0 in diode

D3 0 in2 diode

D4 in2 out diode

.model diode D tt=5e-9

R1 out 0 100

v1 in in2 dc 5 sin(0 10 50 0 0)

1)**TR** analysis(in time)

Graphical user interface, application, histogram

Description automatically generated

2) **DC** analysis

Graphical user interface, application

Description automatically generated

Diagram, schematic

Description automatically generated